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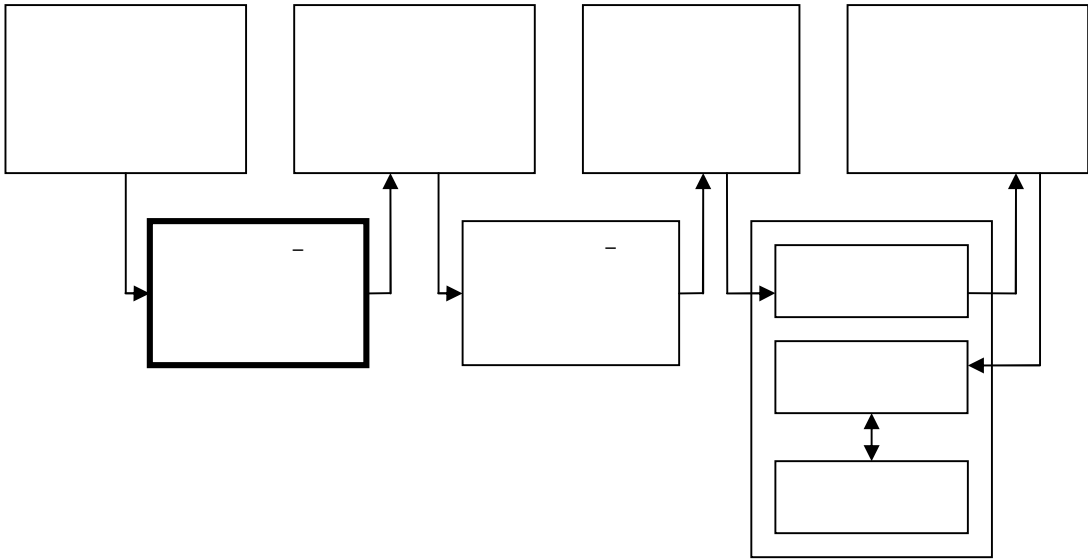
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(target) , (host)
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Windows Linux.

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2.3.	12
2.3.1.	12
2.3.2.	13
2.3.3.	13
2.4.	13
2.4.1.	13
2.4.2.	24
2.4.3.	24
2.5.	32
2.5.1.	32
2.5.2.	35
2.5.3.	35
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EXAMP1: PROC OPTIONS (MAIN);

DCL A BIN FIXED (31) INIT (111B);

DCL B BIN FIXED (31) INIT (100B);

DCL C BIN FIXED (31) INIT (101B);

DCL D BIN FIXED (31);

D = A + B - C;

END EXAMP1;

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1. <PRO> ::= <OPR><TEL><OEN>
 2. <OPR> ::= <IPR>:PROC_OPTIONS(MAIN);
 3. <IPR> ::= <IDE>
 4. <IDE> ::= <BUK> | <IDE><BUK> | <IDE><CIF>
 5. <BUK> ::= A | B | C | D | E | M | P | X
 6. <CIF> ::= 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9
 7. <TEL> ::= <ODC> | <TEL><ODC> | <TEL><OPA>
 8. <ODC> ::= DCL_<IPE>_BIN_FIXED(<RZR>); |
DCL_<IPE>_BIN_FIXED(<RZR>)INIT(<LIT>);
 9. <IPE> ::= <IDE>
 10. <RZR> ::= <CIF> | <RZR><CIF>
 11. <LIT> ::= <MAN>B
 12. <MAN> ::= 1 | <MAN>0 | <MAN>1
 13. <OPA> ::= <IPE>=<AVI>;
 14. <AVI> ::= <LIT> | <IPE> | <AVI><ZNK><LIT> |
<AVI><ZNK><IPE>
 15. <ZNK> ::= + | -
 16. <OEN> ::= END_<IPR>
- :
- "<" ">" -
,
- "::=" - " ",
- "|" - " ",
- "_" - " ",
- "<PRO>" - " ",
- "<OPR>" - " ",
- "<IPR>" - " ",

"<IDE>" - " " ,
 "<BUK>" - " " ,
 "<CIF>" - " " ,
 "<TEL>" - " " ,
 "<ODC>" - " declare" ,
 "<IPE>" - " " ,
 "<RZR>" - " " ,
 "<LIT>" - " " ,
 "<MAN>" - " " ,
 "<OPA>" - " " ,
 "<AVI>" - " " ,
 "<ZNK>" - " " ,
 "<OEN>" - " " .

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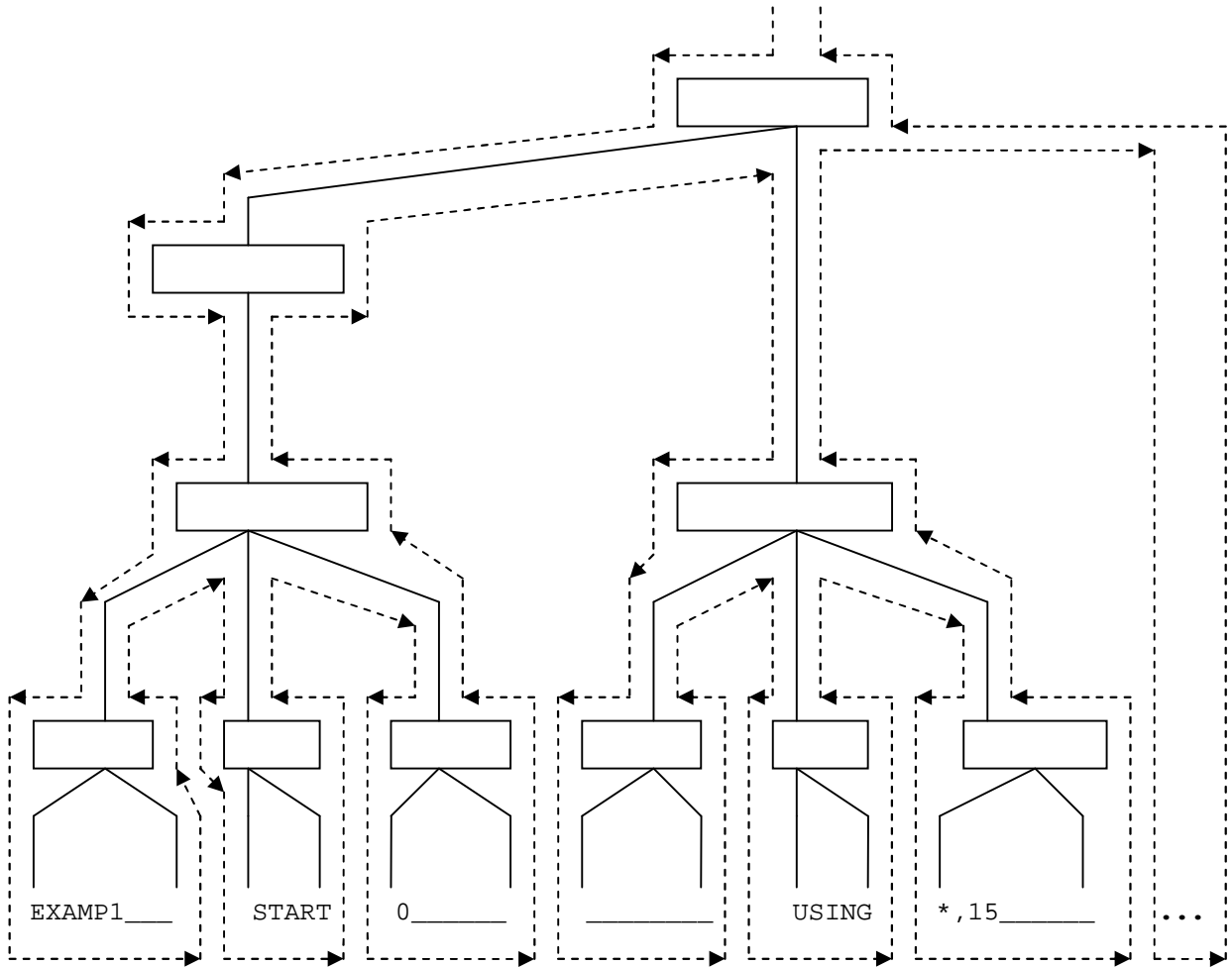
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■ " " (c 10- );
■ " " ( 16- ).
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        ...

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- 3)

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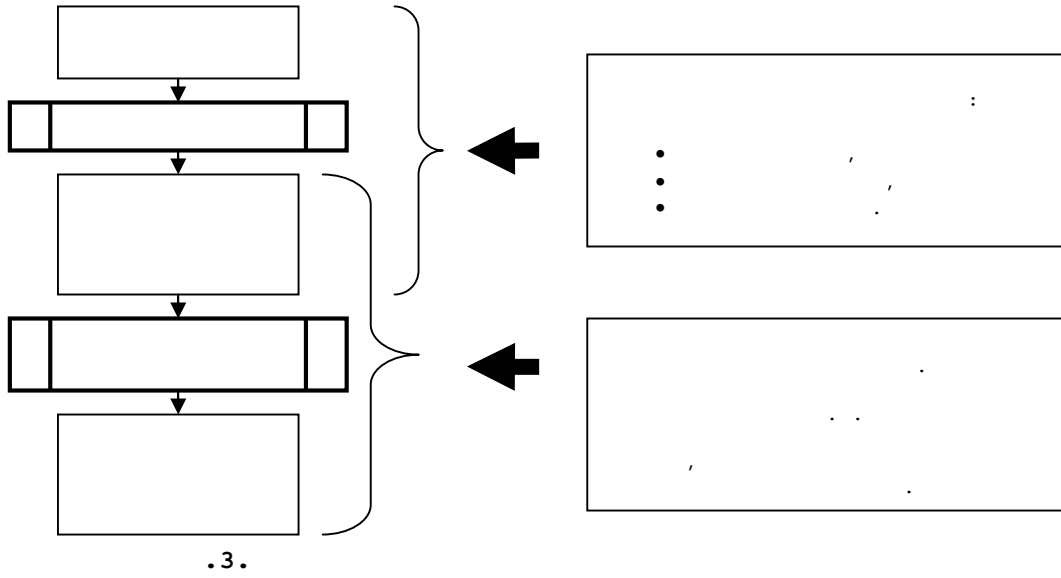
3)

4)

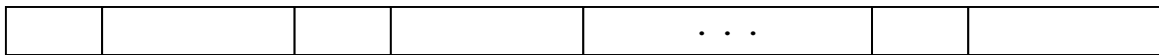
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, SPIS2:

{ ":" , "(" , ")" , ";" , "+" , "-" , "=" }.

2.3.3.

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2.4.

2.4.1.

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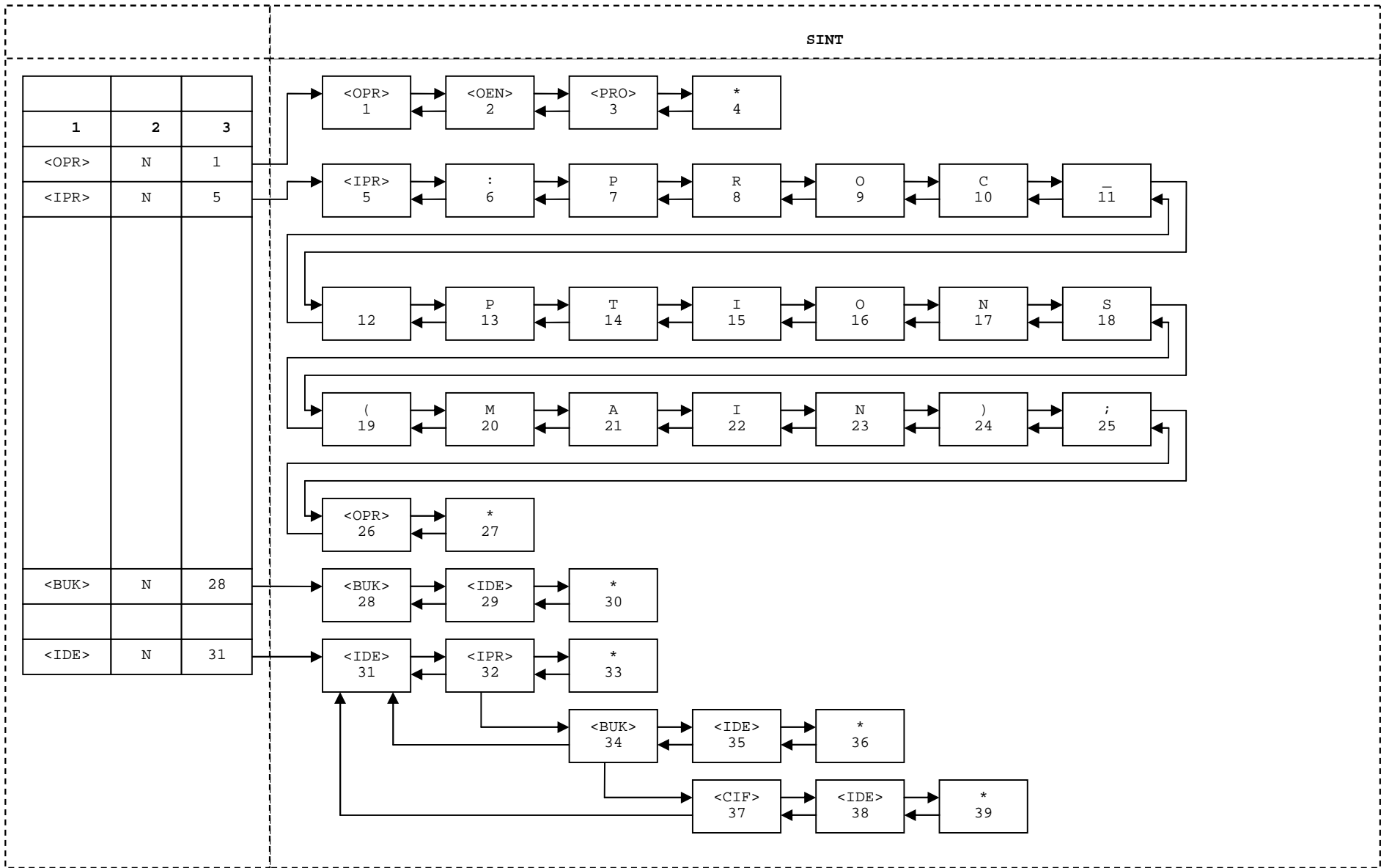
2)

- " - ";
- " - ".

1. <PRO> ::= <OPR><OEN>
2. <OPR> ::= <IPR>:PROC_OPTIONS(MAIM);
3. <IPR> ::= <IDE>
4. <IDE> ::= <BUK> | <IDE><BUK> | <IDE><CIF>
5. <BUK> ::= E | X
6. <CIF> ::= 1
7. <OEN> ::= END_<IPR>;

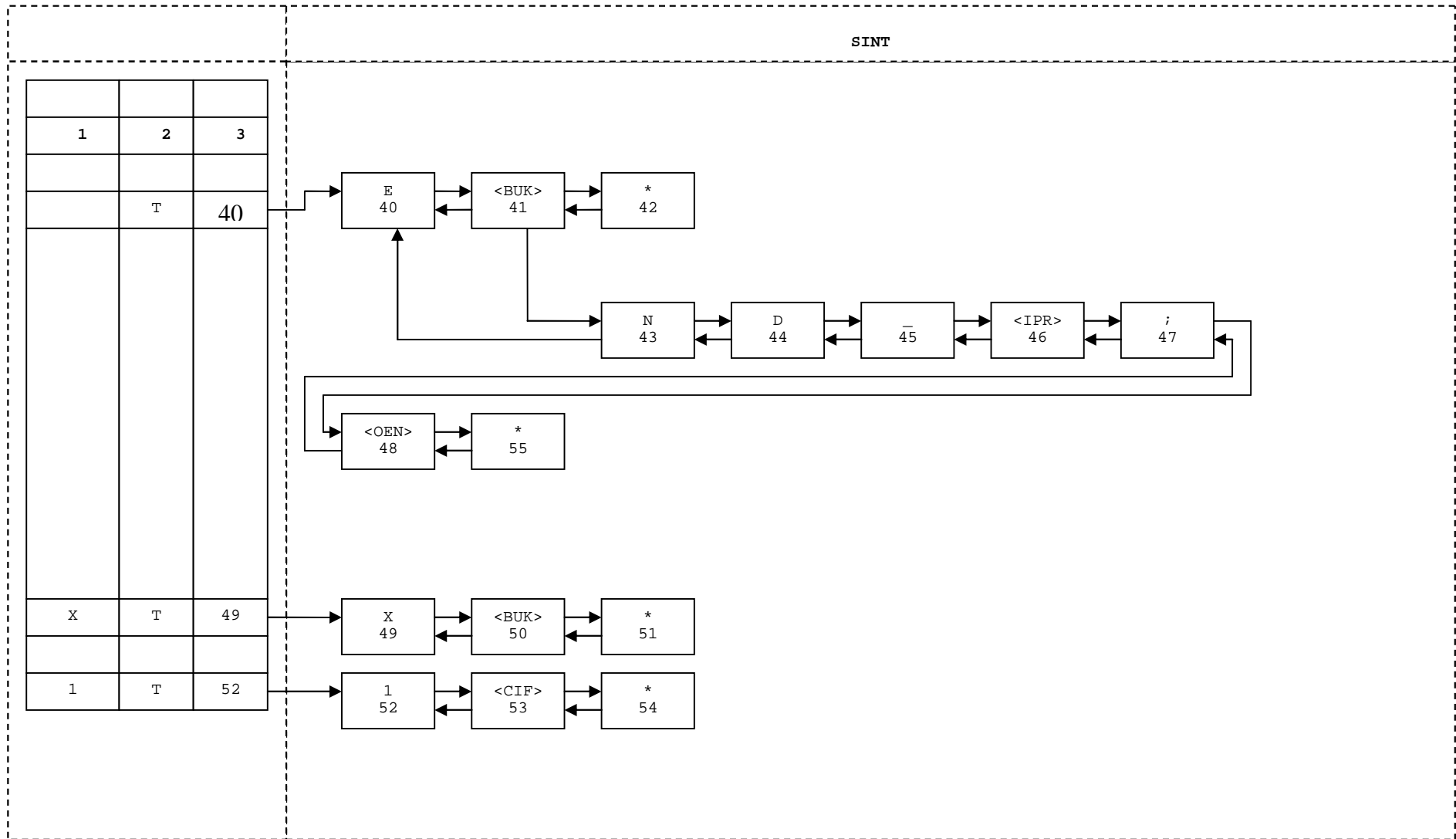
EX1:PROC_OPTIONS(MAIN);END_EX1;

"C"



.6.

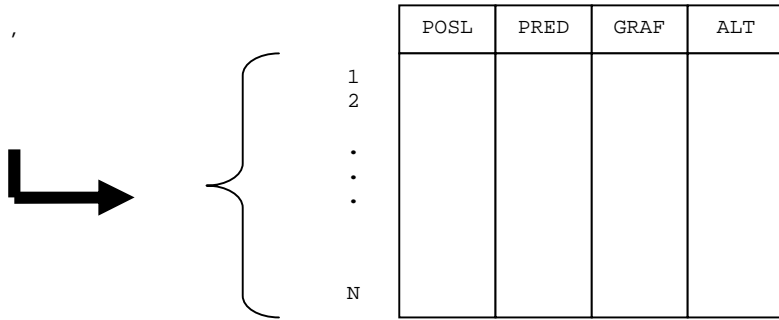
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.7.

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 " " (PRED) ,
 " " (ALT) .
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CEL)

<div style="border: 1px solid black; padding: 2px; display: inline-block;"> 1 . . N </div>	-		SINT
	CEL1	CEL2	CEL3

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<div style="border: 1px solid black; padding: 2px; display: inline-block;"> 1 . . . </div>	-		SINT		SINT
	DST1	DST2	DST3	DST4	DST5

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<CIF>							
<IDE>			1	1			
<IPR>						1	
<OEN>							
<OPR>							1
<PRO>							
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X	1						
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for ( j=1; j < n+1; j++ ) {
  for ( i=1; i < m+1; i++ ) {
    if ( PR [i,j] & (i != j) ) then {
      for ( k=1; k < n+1; k++ ) {
        PR [i,k] |= PR [j,k];
      }
    }
  }
}

```

PR

	<BUK>	<CIF>	<IDE>	<IPR>	<OEN>	<OPR>	<PRO>
<BUK>			<u>1</u>	1		1	1
<CIF>							
<IDE>			<u>1</u>	<u>1</u>		1	1
<IPR>						<u>1</u>	1
<OEN>							
<OPR>							<u>1</u>
<PRO>							
E	<u>1</u>		1	1	1	1	1
X	1		1	1		1	1
1		<u>1</u>					

.12. - PR

2.4.2.

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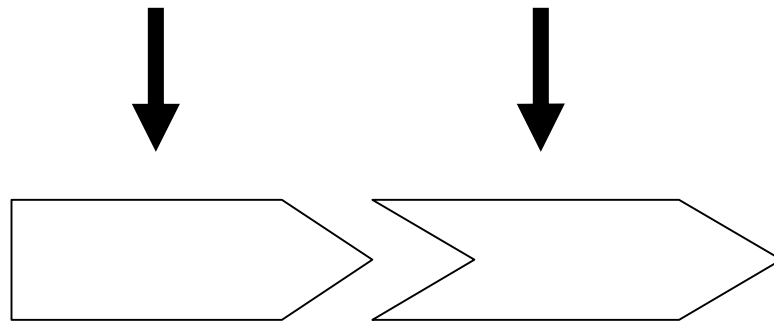
- (STR) , /1
- (BX) ,
- (SINT) ,
- (CEL) ,
- (DOST) ,
- (PR) .

2.4.3.

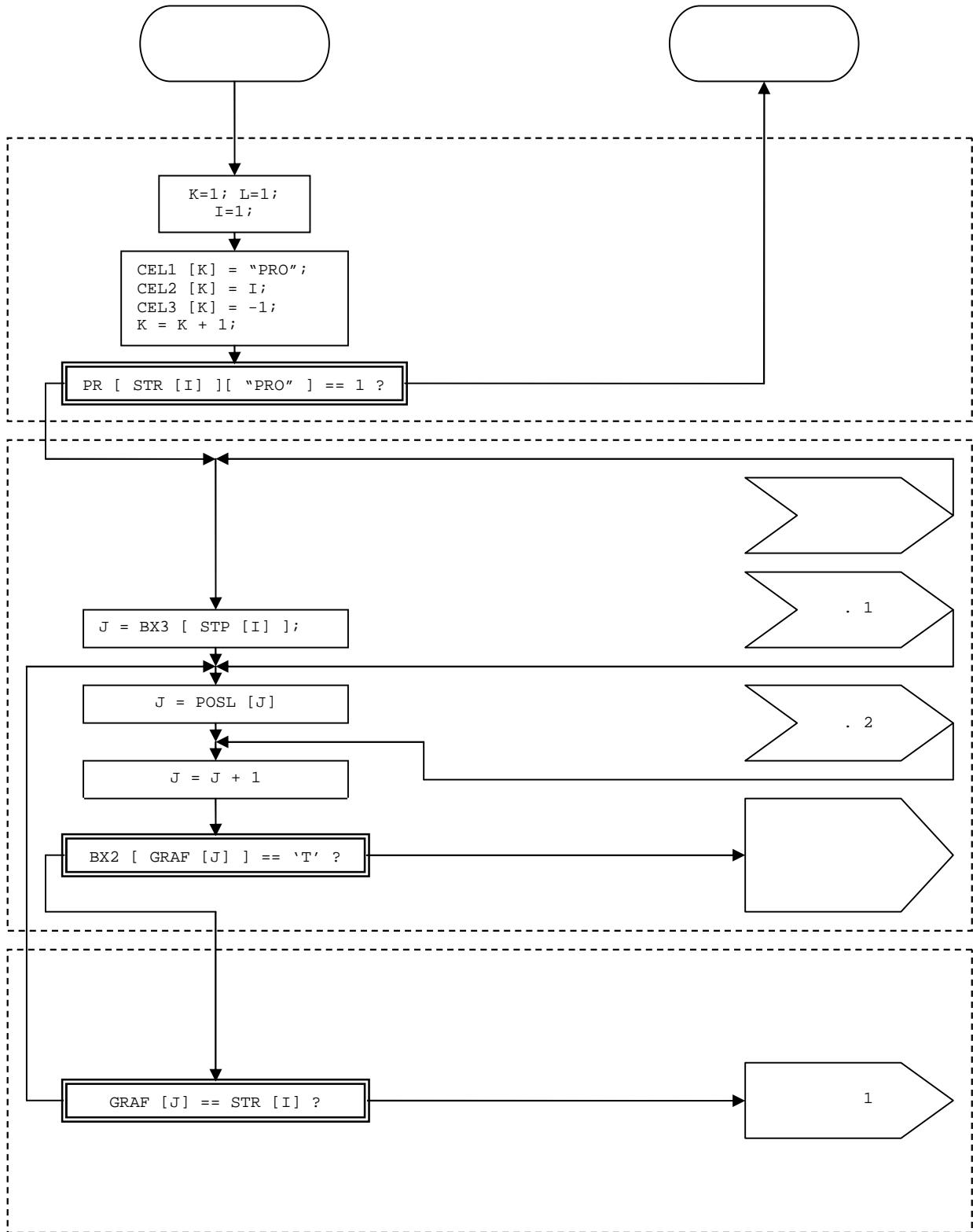
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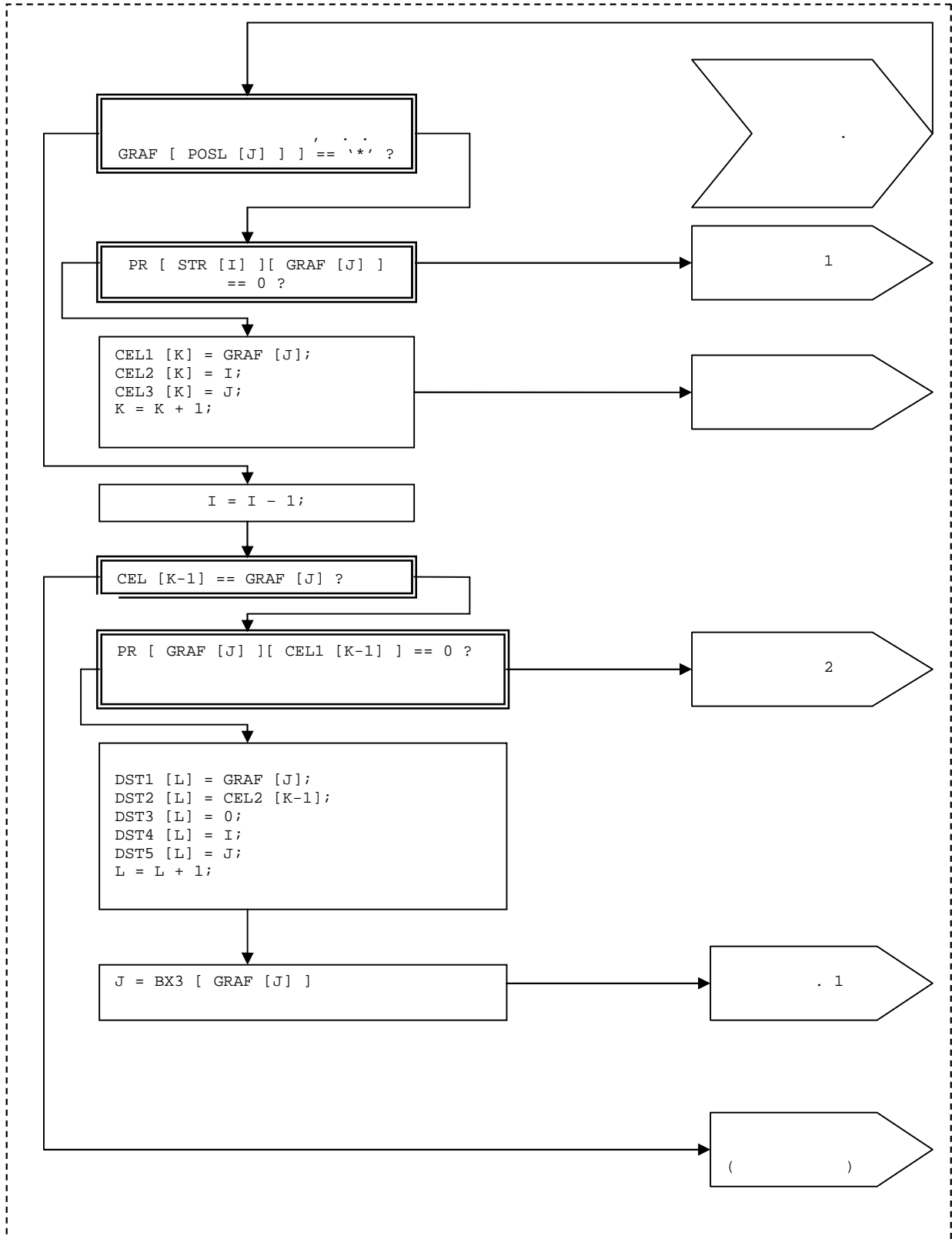
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- (. .15 .16),
- (. .17).

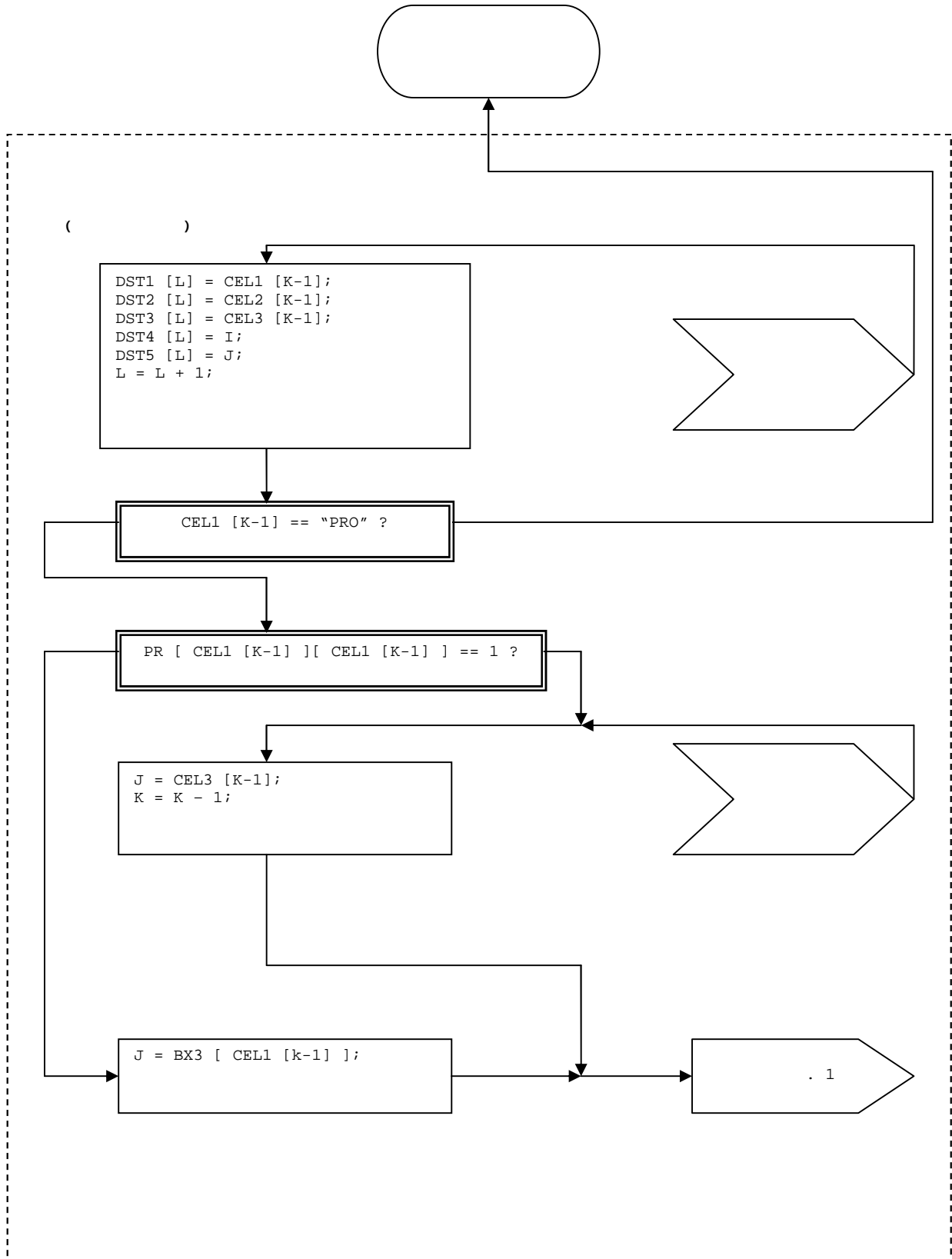
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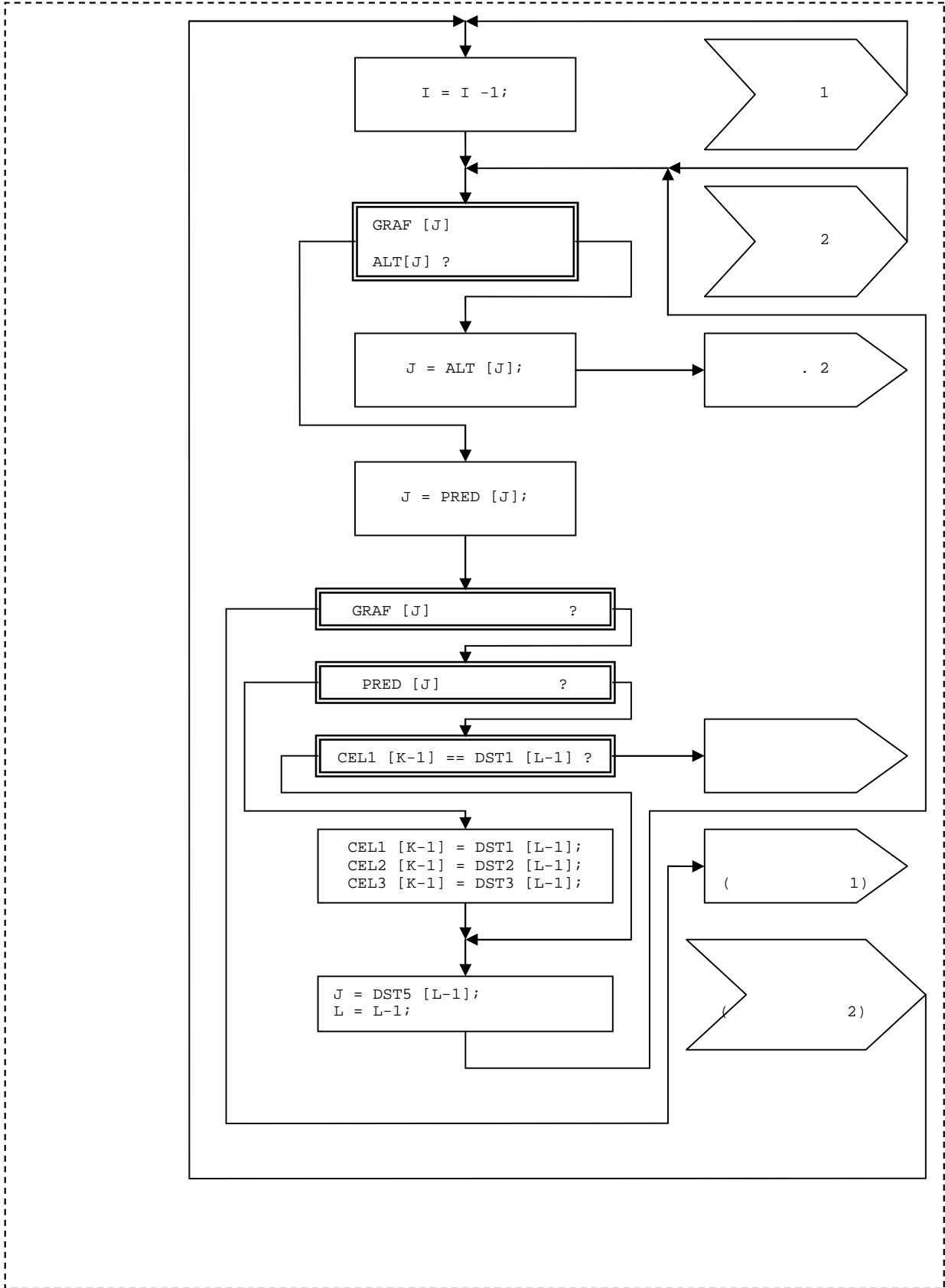


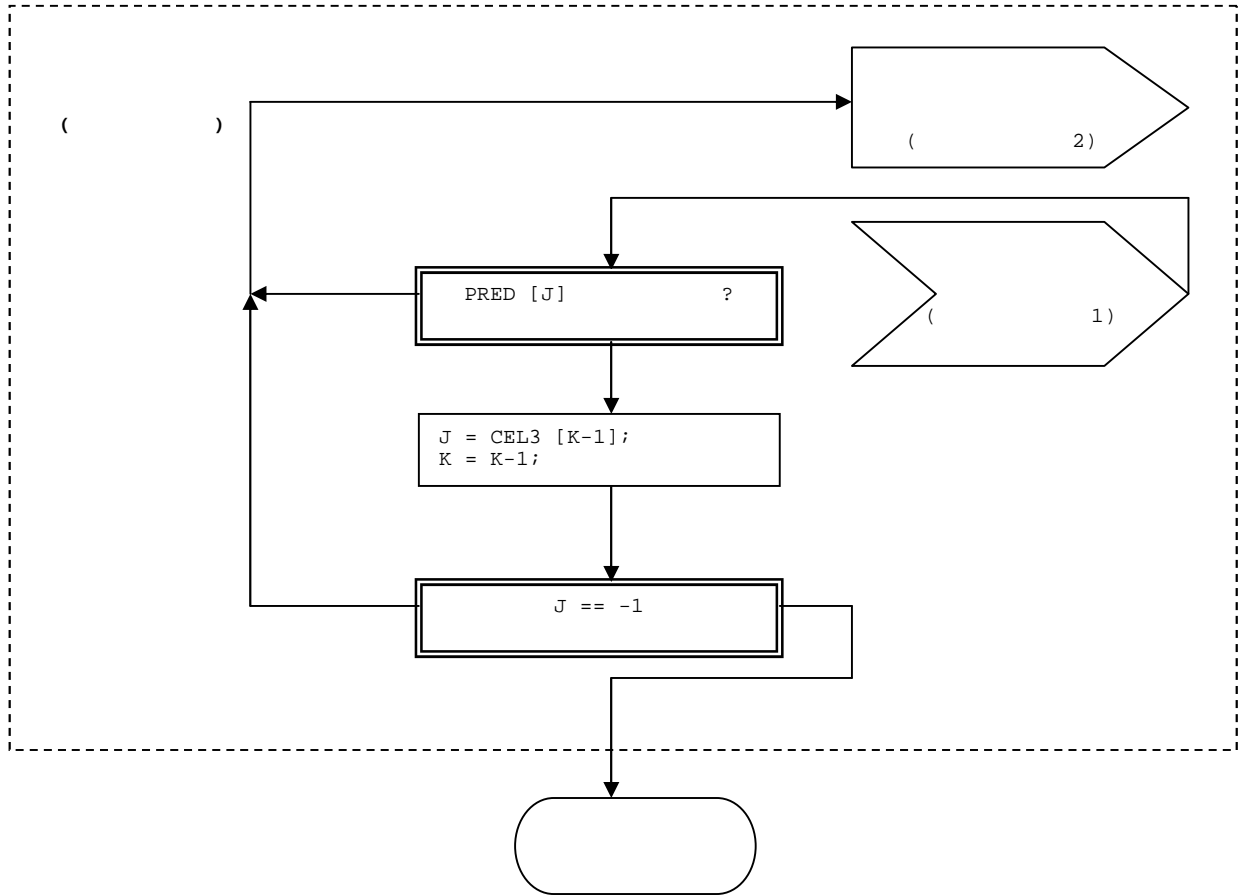




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E	X	1	:	P	R	O	C	_	O	P	T	I	O	N	S	(M	A	I	N)	;
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E	N	D	_	E	X	1	;
24	25	26	27	28	29	30	31

DST

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			SINT		SINT
	DST1	DST2	DST3	DST4	DST5
1	BUK	1	0	1	41
2	IDE	1	0	1	29

3	IPR	1	0	1	32
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3	BUK	2	34	2	50
4	IDE	1	0	2	35

5	IPR	1	0	2	32
---	-----	---	---	---	----

5	CIF	3	37	3	53
6	IDE	1	0	3	38
7	IPR	1	0	3	32
8	OPR	1	0	23	26
9	BUK	28	0	28	41
10	IDE	28	0	28	29

11	IPR	28	46	28	32
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11	BUK	29	34	29	50
12	IDE	28	0	29	35

13	IPR	28	46	29	32
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13	CIF	30	37	30	53
14	IDE	28	0	30	38
15	IPR	28	46	30	32
16	OEN	24	2	31	48
17	PRO	1	-1	31	31

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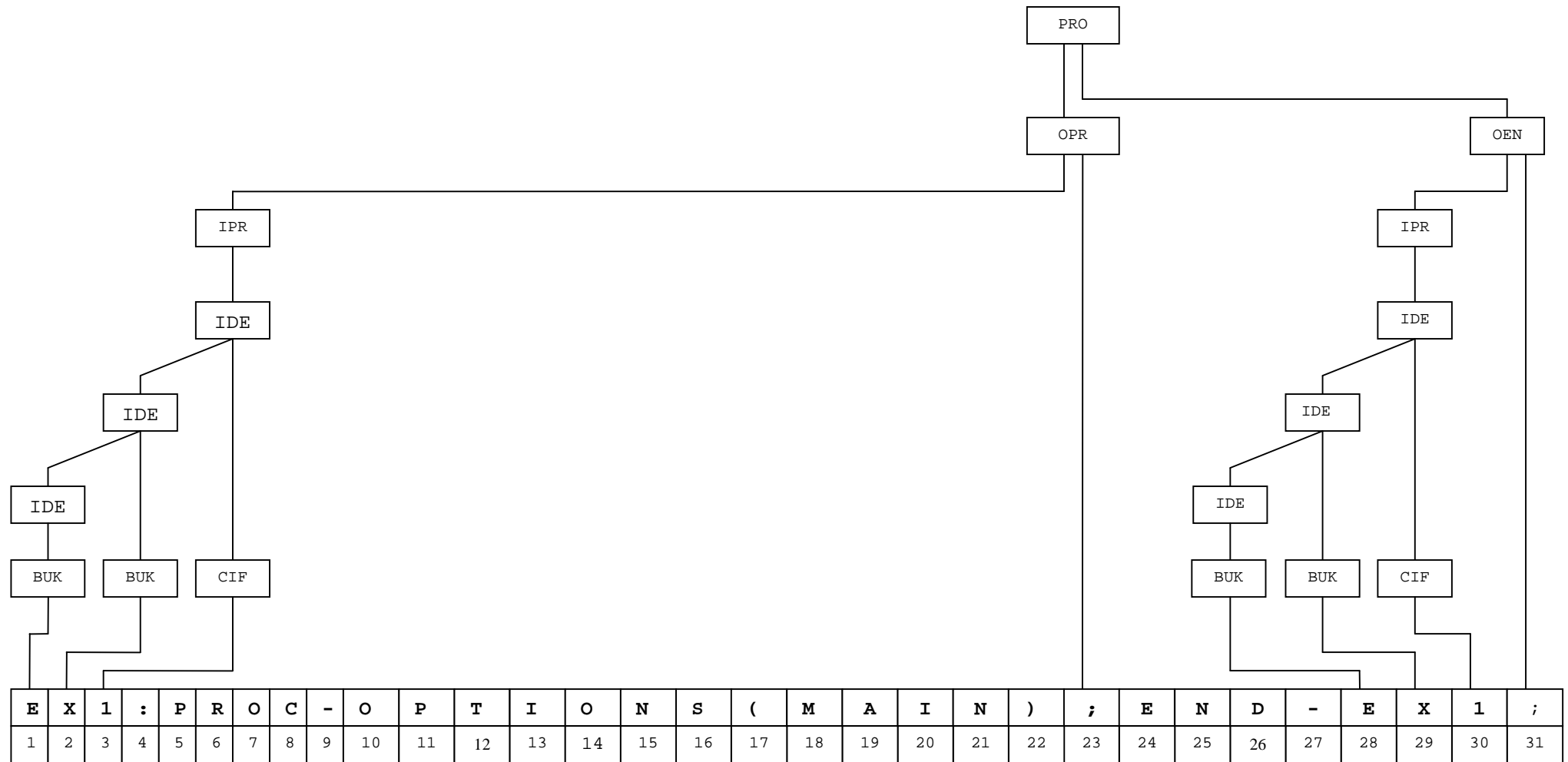
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BUK																														
E	X	1	:	P	R	O	C	-	O	P	T	I	O	N	S	(M	A	I	N)	;	E	N	D	-	E	X	1	;
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

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IDE																														
BUK																														
E	X	1	:	P	R	O	C	-	O	P	T	I	O	N	S	(M	A	I	N)	;	E	N	D	-	E	X	1	;
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

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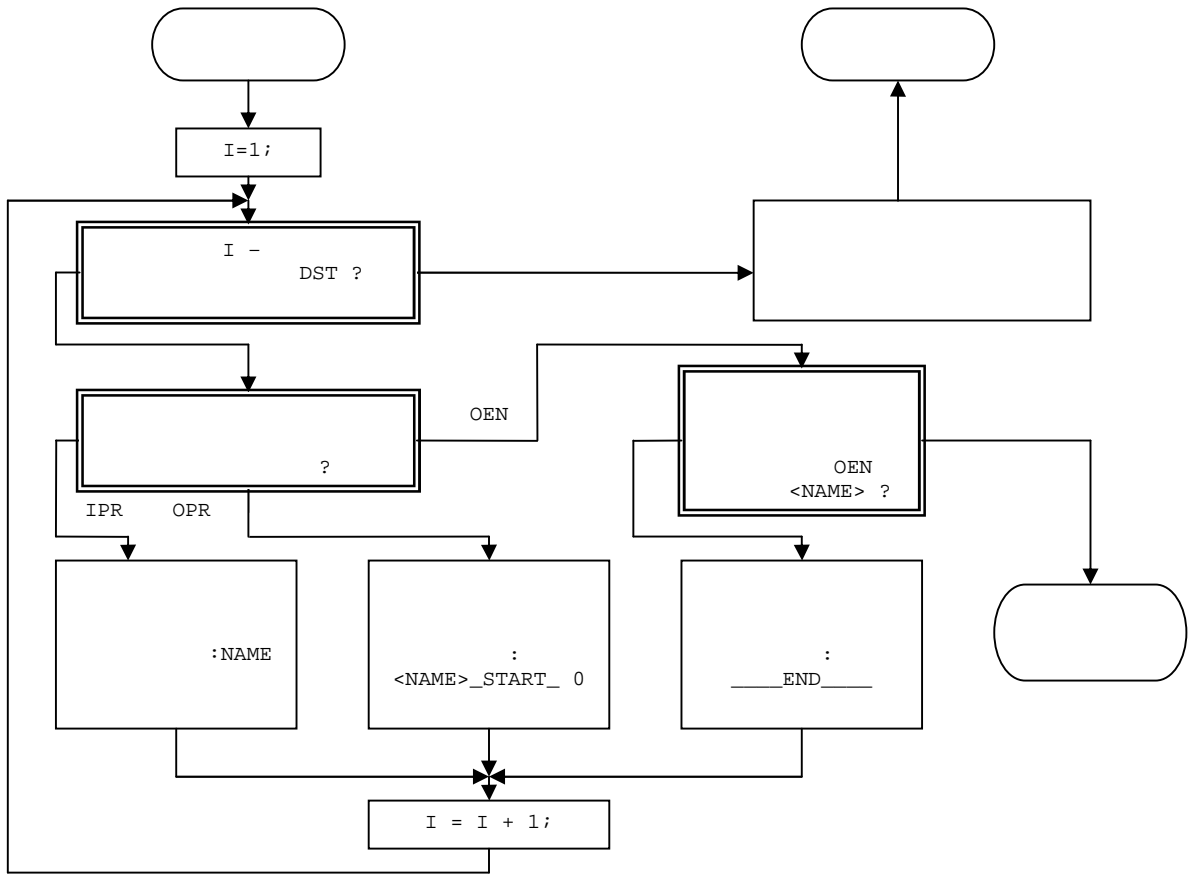
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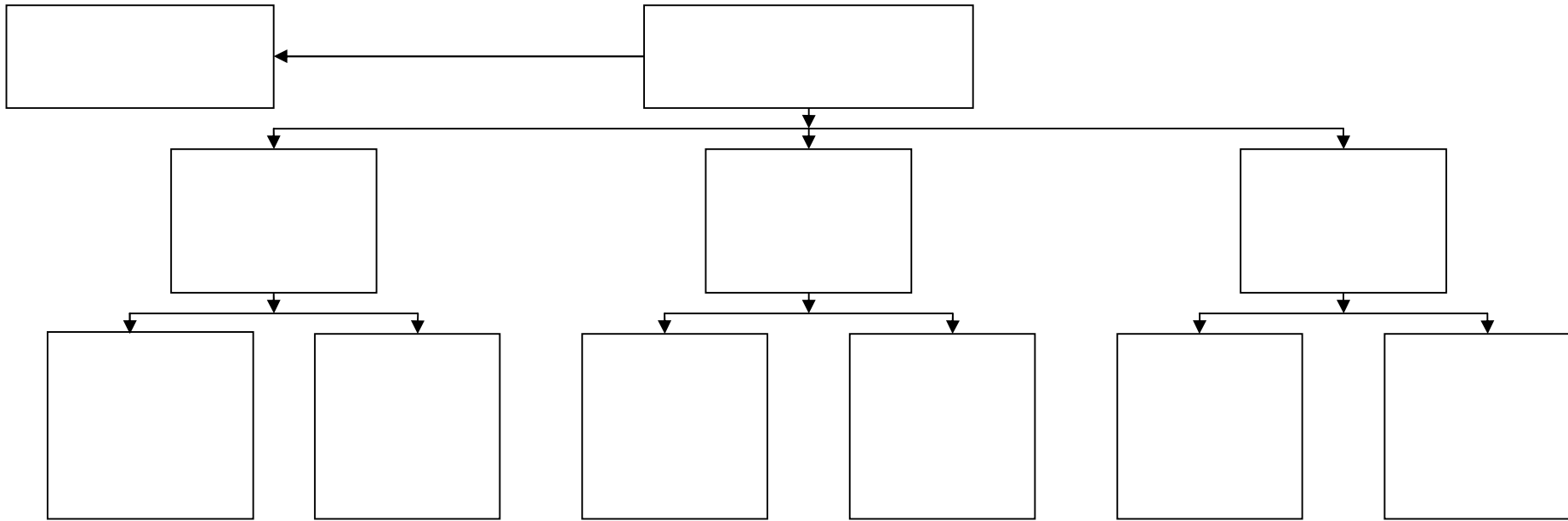
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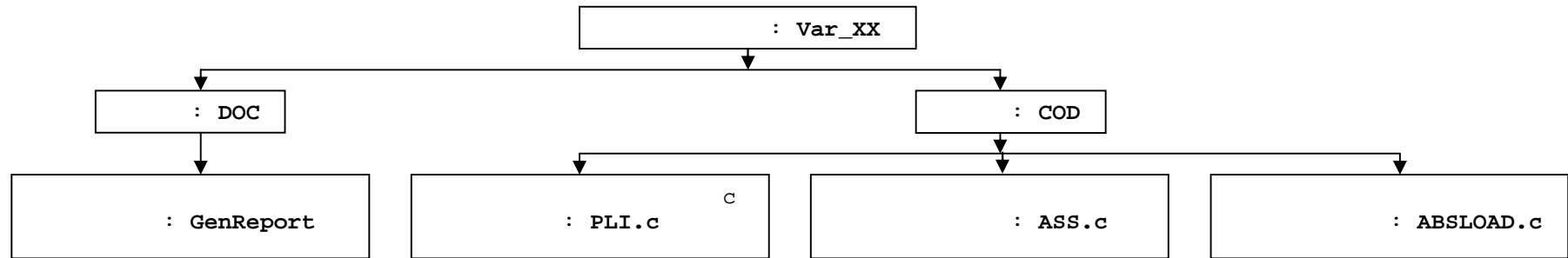
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